
1963 Solid State Circuits Conference Digest Technical

iee journal titles and reference abbreviations title ... - exploratory solid-state computational devices and circuits, *iee journal on iee j. explor. solid-state computat. solid-state computat. fuzzy systems*, *iee transactions on iee trans. fuzzy syst.* **iee journal of solid-state circuits, vol. 42, no. 9 ...** - *iee journal of solid-state circuits, vol. 42, no. 9, september 2007 1963 a 5-6 ghz 1-v cmos direct-conversion receiver with an integrated quadrature coupler 1963 , volume v.14 n.7-8 , issue march/april-1963* - journal hewlett-packard technical information from the -hp- laboratories published by the hewlett-packard company, 1501 page mill road, palo alto, california mar. **effects of oxide traps on the mos capacitance - nanolund** - integrated circuits design course notes., 1963. [5] dicken, h. k., parasitics effects in integrated circuits, presented at the feb 1963, internat'l solid-state circuits conf. **lam - defense technical information center** - 26 radc-tdr-63-83 january 1963 third quarterly progress report on solid-state circuits by s. hamilton, w. w. heinz, s. okwit, e. w. sard, and k. siegel **the evolution of oversampling analog-to-digital converters** - solid-state circuits council ! established in may 1966, the council preceded the solid-state circuits society ! founded to sponsor the journal of solid-state circuits (jssc) **the flash adc - engineering** - *iee solid-state circuits magazine summer 2017 9 f flash analog-to-digital converters (adcs) find wide application both as stand-alone components and as building blocks of more complex systems. this architecture dates back to at least the early 1960s. for example, in a patent filed in 1963, stephenson [1] describes the "parallel" adc as a known technique. in this article, we study the ... a precise four-quadrant multiplier with subnanosecond response* - 366 1968 another problem of the "differential-amplifier" multiplier, analyzed in [7], is the nonlinear response with respect to the base-voltage input. **1. joseph i. mundy professor of engineering (research ...** - 1969-1972 solid state physicist general electric research 1966-1969 graduate student renselaer 1963-1966 microwave engineer ge microwave dept. **one-dimensional discrete-time cnn with multiplexed ...** - 764 *iee transactions on circuits and systems-i: fundamental theory and applications, vol. 47, no. 5, may 2000 also by blauschild in 1981 in a variable-gain amplifier product [9], [10]. 1996 iee international solid state circuits conference ...* - 1996 *iee international solid state circuits conference digest of technical papers serial signal & image processing : an international journal (sipij) vol.5, no.1, february 2014 raad ahmed hadi1, 2, ghazali the electronic watch and low-power circuits* - atlantic ocean, in this strange new domain for them called solid-state electronics. led by gerard bauer, the dynamic director of the federation of the swiss watch industry (fh), a research group representing **stability considerations for pnpn current-voltage measurements** - appendix a stability considerations for pnpn current-voltage measurements in characterizing the switching and holding points with the parameter analyzer, it is often possible to measure a portion of the **1963 , volume v.15 n.3 , issue nov-1963 - hp labs** - november, 1963 a new multi-purpose digital voltmeter 1 he accuracy and speed with which digital volt meters make dc voltage measurements have made these instruments extremely useful in a variety of applications. one of their attractive properties is that the digital readout has greater resolution than a meter and is less likely to be misread, particularly by unskilled personnel. also, data ...

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